

FIG. 1
(Prior Art)

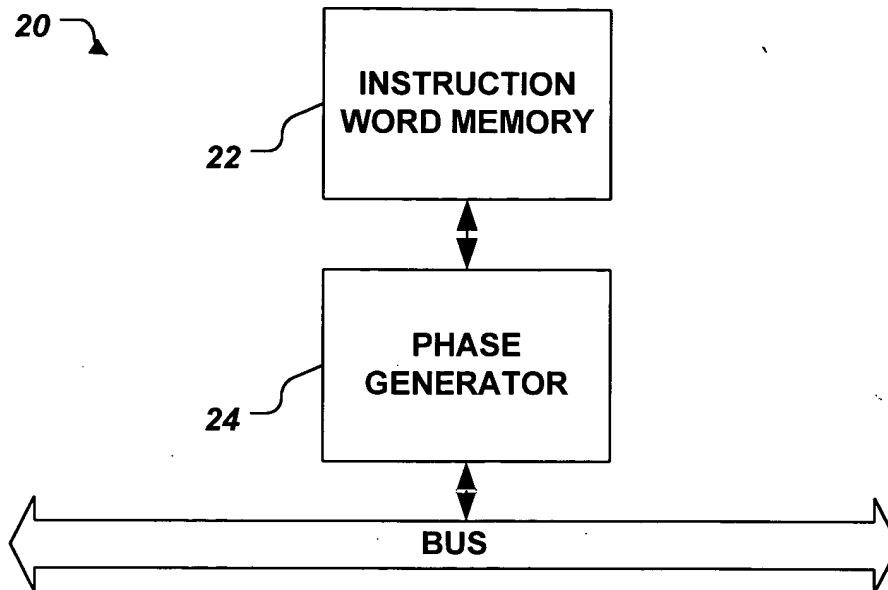


FIG. 2

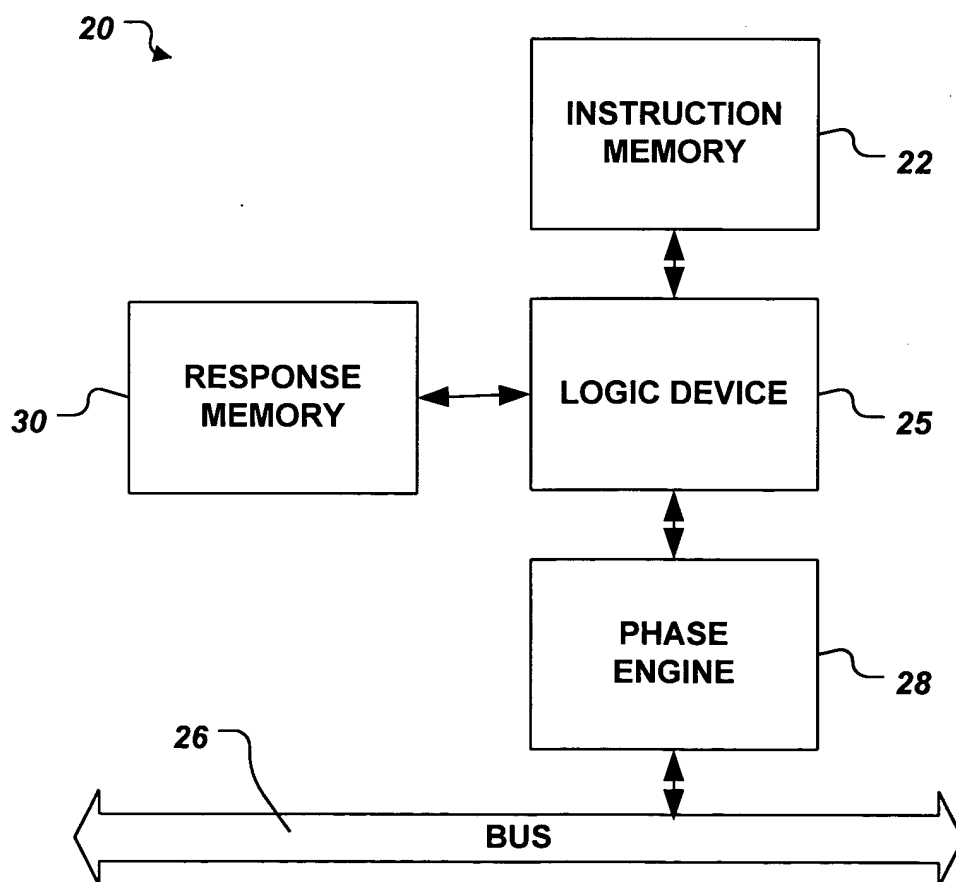

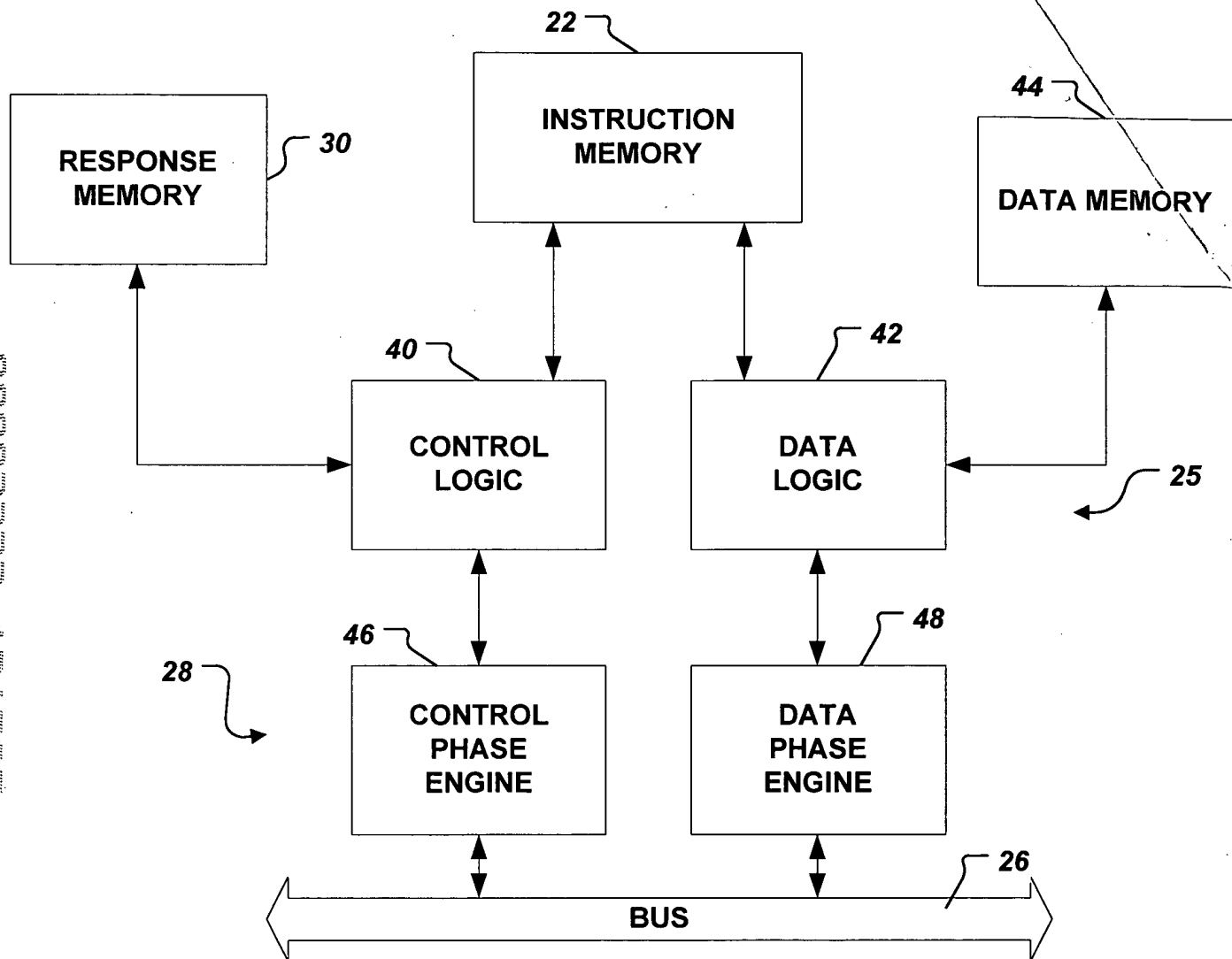


FIG. 3

20 



RESPONSE MEMORY

30

INSTRUCTION MEMORY

CONTROL LOGIC

40 -

DATA LOGIC

42

DATA MEMORY

44

25

28

CONTROL PHASE ENGINE

46 -

**DATA
PHASE
ENGINE**

48

BUS

26

FIG. 4

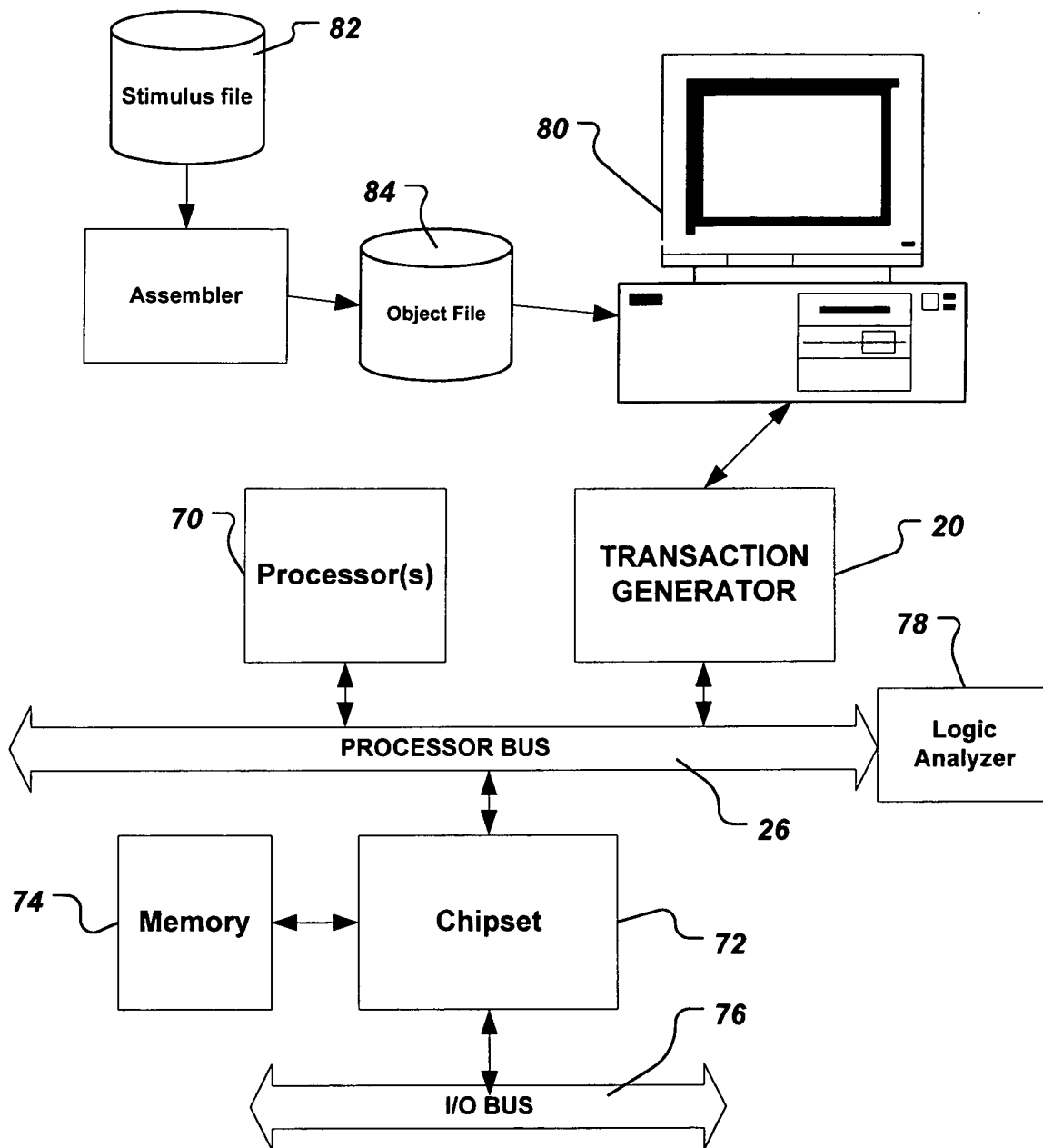


FIG. 6

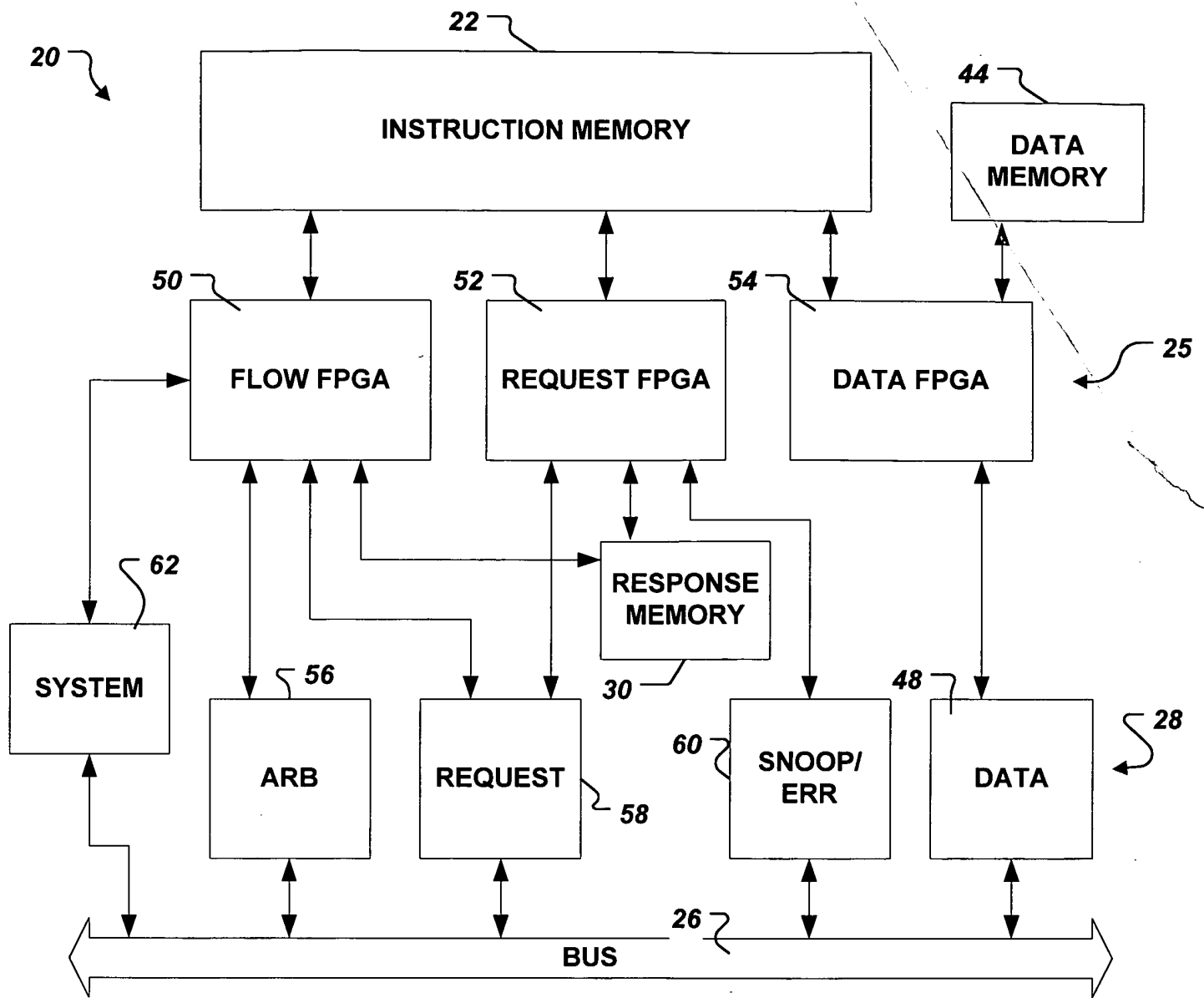


FIG. 5

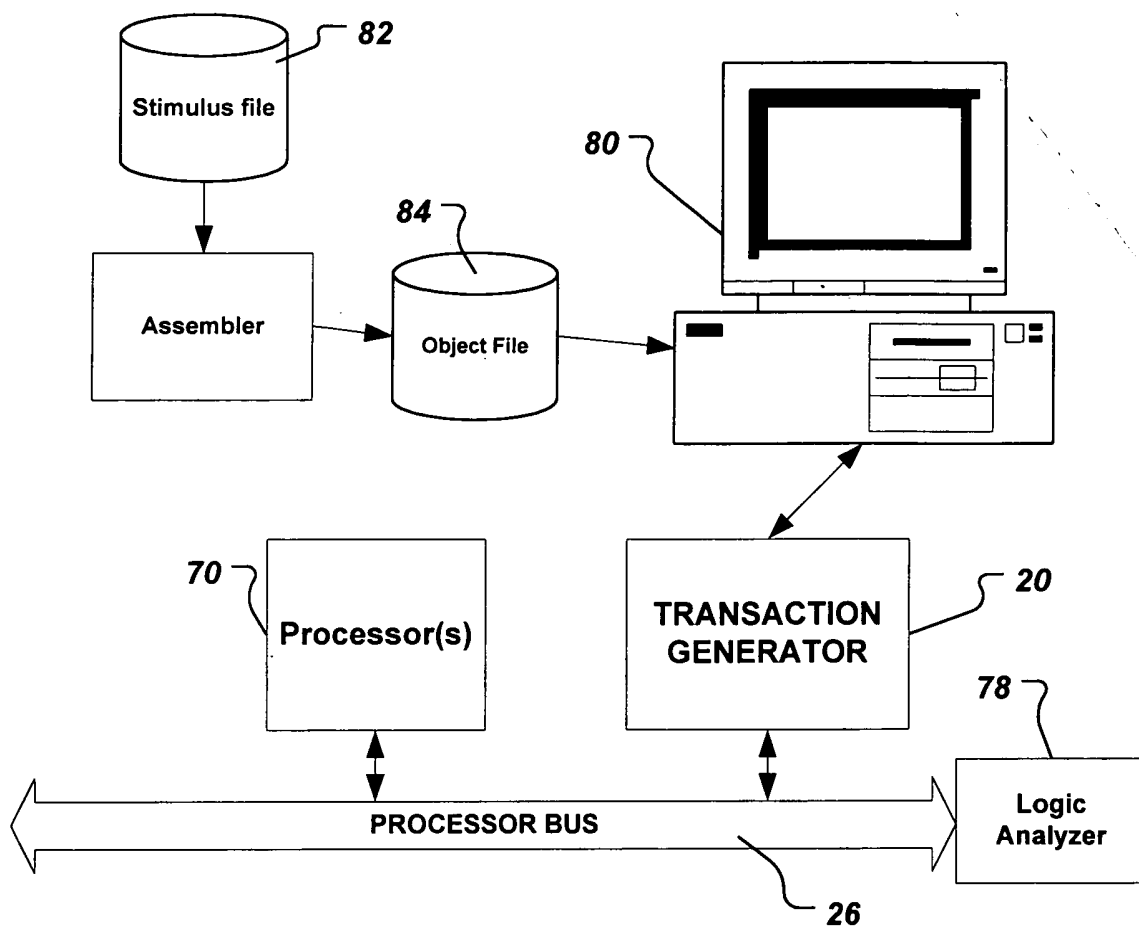


FIG. 7